# Programming in HMMM 

BBM103 Introduction to Programming Lab 1
Week 3

Fall 2019

## Von Neumann Architecture

- A program (a list of instructions) is stored in the main memory.
- Stored Program Concept
- Instructions are copied (one at a time) into the instruction register in the CPU for execution.



## Von Neumann Architecture

## processing


central processing unit registers

## program

## RAM


$\simeq 2$ General-purpose register, r2

## Programs are stored in memory in machine language.

| 0 | 0000000100000001 |
| :---: | :---: |
| 1 | 1000001000010001 |
| 2 | 0110001000100001 |
| 3 | 0000001000000010 |
| 4 | 0000000000000000 |
| 5 6 | (all bits) |

## The Power of the Stored Program

- A program written in machine language is a series of binary numbers representing the instructions stored in memory.
- The stored program concept is a key reason why computers are so powerful:
- Running a different program does not require large amounts of time and effort to reconfigure or rewire hardware; it only requires writing the new program to memory.


## Assembly Language

- Assembly language is a human-readable machine language.
- Instead of programming in binary (0's and 1's), it is easier to use an assembly language.
- An assembler is a computer program that interprets software programs written in assembly language into machine language.


## The Harvey Mudd Miniature Machine (HMMM)

- Hmmm (Harvey Mudd Miniature Machine) is a 16 -bit, 23 -instruction simulated assembly language with $2^{8}=25616$-bit words of memory.
- In addition to the program counter and instruction register, there are 16 registers named r0 through r15.

Hmmm assembly code

| 0 | read | $r 1$ |  |  |
| :--- | :--- | :--- | :--- | :--- |
| 1 | read | r2 |  |  |
| 2 | mul | r1 | r1 | r2 |
| 3 | setn | r2 | 2 |  |
| 4 | div | r1 | r1 | r2 |
| 5 | write | r1 |  |  |
| 6 | halt |  |  |  |

Corresponding instructions in machine language

$\square$| 0000 | 0001 | 0000 | 0001 |
| :--- | :--- | :--- | :--- |
| 0000 | 0010 | 0000 | 0001 |
| 1000 | 0001 | 0001 | 0010 |
| 0001 | 0010 | 0000 | 0010 |
| 1001 | 0001 | 0001 | 0010 |
| 0000 | 0001 | 0000 | 0010 |
| 0000 | 0000 | 0000 | 0000 |

## The Harvey Mudd Miniature Machine (HMMM)

## CPU

central processing unit registers

random access memory locations


## The Harvey Mudd Miniature Machine (HMMM)

read r1
write r2
setn r1 $42 \quad$ reg1 $=42$
addn r 1 -1
add r3 r1 r2
sub r3 r1 r2
mul r2 r1 r1
div r1 r1 r2
reads from keyboard into reg1
outputs reg2 onto the screen
reg1 = reg1 - 1 anything from -128 to 127
a shortcut
reg3 = reg1 + reg2
reg3 = reg1 - reg2
reg2 $=$ reg1 * reg1
reg1 = reg1 / reg2

## The Harvey Mudd Miniature Machine (HMMM)

| Instruction | Description | Aliases |
| :---: | :---: | :---: |
| System instructions |  |  |
| halt | Stop! |  |
| read XX | Place user input in register $\mathrm{X} X$ |  |
| write XX | Print contents of register rX |  |
| nop | Do nothing |  |
| Setting register data |  |  |
| setn rX N | Set register XX equal to the integer $\mathrm{N}(-128$ to +127$)$ |  |
| addn XX N | Add integer $\mathrm{N}(-128$ to 127) to register XX |  |
| copy rX rY | Set $r X=r Y$ | mov |
| Arithmetic |  |  |
| add XX rY r 2 | Set $r X=r Y+r Z$ |  |
| sub $r X r Y ~ r Z ~$ | Set $r X=r Y-r Z$ |  |
| neg $x \times r Y$ | Set $r X=-r Y$ |  |
| mul $x \times r y r 2$ | Set $r X=r Y$ * $r 2$ |  |
| div xX rY r Z | Set $\mathrm{XX}=\mathrm{rY} / \mathrm{r} 2$ (integer division; no remainder) |  |
| $\bmod \times X \times Y \times Z$ | Set $r X=r Y$ \& $r 2$ (returns the remainder of integer division) |  |
| Jumps! |  |  |
| jumpn $\mathbb{N}$ | Set program counter to address N |  |
| jumpr XX | Set program counter to address in rX | jump |
| jeqza rX N | If $\mathrm{rX}==0$, then jump to line N | jeqz |
| jnezn $\mathrm{x} \times \mathrm{N}$ | If $\mathrm{x} \times!=0$, then jump to line N | jnez |
| jgtzn rX N | If $\mathrm{rX}>0$, then jump to line $\mathbb{N}$ | jgtz |
| jltzn rX N | If XX < 0, then jump to line $\mathbb{N}$ | jltz |
| calln xX N | Copy the next address into XX and then jump to mem. addr. N | call |
| Interacting with memory (RAM) |  |  |
| loadn rX N | Load register rX with the contents of memory address N |  |
| storen XX N | Store contents of register XX into memory address N |  |
| loadr rX rY | Load register rX with data from the address location held in reg. YY | 1oadi, load |
| storer $r$ X rY | Store contents of register XX into memory address held in reg. rY | storei, store |

## Hmmm

the complete reference

```
At
www.cs.hmc.edu/~cs5grad/cs5/hmmm/ documentation/documentation.html
```


## Example \#1:

Screen
6 (input)


## Example \#1 (cont.):

Screen


## Jumps in HMMM

Jeqzn $r 142$ IF r1==0 THEN jump to line number 42
J̇gtzn r1 42 IF r1>0 THEN jump to line number 42
J1tzn r1 42 IF r1<0 THEN jump to line number 42
Jnezn r1 42 IF r1 != 0 THEN jump to line number 42

Unconditional jump
jumpn 42 Jump to program line \# 42

Indirect jump
jumpr r1 Jump to the line\# stored in r1

## Example \#2:

Screen


## RAM



## What function does this program implement?

## Exercise

1. Write a Hmmm program to compute the following for $\mathbf{x}$ given as user input and output the result to the screen:
a) If $\mathbf{x}<0$
b) else if $x>0$
c) else
$3 x-4$
$\mathrm{X} / 5$
$x^{2}+10 / 5$
